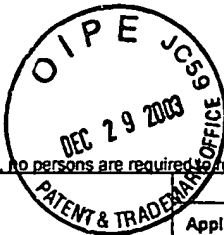


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				Application Number	10/728,068
				Filing Date	December 4, 2003
				First Named Inventor	Steinmann et al.
				Group Art Unit	2825
Examiner Name	NGHIA M. DUAN				
Sheet	1	of	1	Attorney Docket No.	TI-36776

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
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NMD	CA	"Thermal Properties of Very Fast Transistors", RICHARD C. JOY and E.S. SCHLIG, IEEE Transactions on Electron Devices, Volume ED-17, No. 8, August, 1970, 9 pgs.		
NMD	CB	"A Complete High-Speed Voltage Output 16-Bit Monolithic DAC", JIMMY R. NAYLOR, IEEE Journal of Solid-State Circuits, Volume SC-18, No. 6, December, 1983, 7 pgs.		
	CC			
	CD			
	CE			
	CF			
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Examiner Signature		Date Considered	01/31/2006
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Fax Cover Sheet

Date: 01 Feb 2006

To: Thomas G. Eschweller	From: Nghia M. Doan
Application/Control Number: 10/728,068	Art Unit: 2825
Fax No.: 216-502-0601	Phone No.: 571-272-5973
Voice No.: 216-502-0600	Return Fax No.: (571) 273-5973
Re:	CC:

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Comments:
February 01, 2006

Dear Mr. Thomas,

As conversation with you over the phone regarding the IDS filed on 12/29/2003. It has been considered by Examiner. If you need any further assistant, please contact me as my contact informatio above.

Best Regards,

Examiner

Nghia Doan

Number of pages 02 **including this page**

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